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US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database Database: EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index **IBM Technical Disclosure Bulletins** (CMOS near invertor or ring oscillator) and (FET or field effect) and (junction near temperature) Term: Display: 10 Documents in Display Format: |- Starting with Number | 1 Generate: C Hit List @ Hit Count C Side by Side C Image Clear · · Search History **Purge Queries** Printable Copy Create Case DATE: Friday, April 13, 2007 Set Name Query side by side DB=PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=ADJ (CMOS near invertor or ring oscillator) and (FET or field effect) and (junction near 14 L6 L6 temperature) L5 (CMOS) and (FET or field effect) and (junction near temperature) 271 L5 L4 (CMOS near inverter) same (FET or field effect) same (junction near temperature) 0 L4 DB=PGPB, USPT, USOC, EPAB, JPAB; PLUR=YES; OP=ADJ <u>L3</u> · L2 and (CMOS near inverter) 41 L3 1488 L2 L2 L1 and (FET or field effect) (374/170,172,178,141,137,166,163,183;257/48;327/512,513,100,581;702/130,133)!<u>L1</u> [CCLS]

END OF SEARCH HISTORY